



**NATIONAL INSTITUTE OF TECHNOLOGY ROURKELA**  
**SPONSORED RESEARCH, INDUSTRIAL CONSULTANCY & CONTINUING EDUCATION**

**NITR/SR/2023/Advt-22/ST-ISRO/EC/089/L/011 dt. 11.05.2023**

Applications are invited on prescribed format (On-line) for the temporary post of "Junior Research Fellow (JRF)" for the Project entitled "Design and development of Mixed Signal SoC for Sensor Application in SCL 180nm." Selected candidate may enroll in Ph.D. / M. Tech (R) Programme in the **Department of Electronics and Communication Engineering, National Institute of Technology Rourkela** based on Institute norms and regulations.

1.	Project Code	<b>SR/22/ST-ISRO/EC/089</b>
2.	Name of the Temporary Post	Junior Research Fellow - 02
3.	Name of the Research Project	"Design and development of Mixed Signal SoC for Sensor Application in SCL 180nm".
4.	Name of the Sponsoring Agency	ISRO, RESPOND Programme
5.	Tenure of the Project	03 years( from 23.03.23 to 22.03.26)
6.	Tenure of the Assignment	Initial for a period of one year (Extendable to three years upon satisfactory performance)
7.	Job Description	To carry out the research work (VLSI Design), in the field of ADC, DAC Design and RISC-V Processor design & to visit ISRO for follow up.  Scope to enroll as a Ph. D. Scholar in Department of Electronics and Communication Engineering (has to satisfy Institute / Department Eligibility Criteria)
8.	Consolidated monthly compensation / Fellowship	INR 36,580 (1st & 2nd year) and INR 41,300 (3rd year)
9.	Essential Qualifications and experience	M. Tech/B. Tech in Electronics and Communication Engineering or related discipline with 65% marks or 7.0 CGPA, along with Institute norms.
10.	Desirable Qualifications / Exp.	Preferred with Valid GATE Score
11.	Age	As per Institute norms.
12.	Accommodation	Bachelor / Married accommodation in the Institute may be provided subject to availability

For technical information on the project, the candidate may contact the Principal Investigator at the following address:

Name : Prof. Santanu Sarkar  
Address : Department of Electronics and Communication Engineering  
National Institute of Technology Rourkela, Odisha 769 008.  
Tel. No. : 0661-246 2472  
Mobile : 9439783886  
E-mail : [sarkars@nitrkl.ac.in](mailto:sarkars@nitrkl.ac.in)

Date and Time for **On-line** Interview: **16.06.2023 at 10:00 HRS**

Place of Interview: **Department of Electronics and Communication Engineering, NIT Rourkela**

Eligible candidates may apply within **31.05.2023, 2359 HRS**. The candidates are required to send the complete filled up application form (Soft copy) to [sarkars@nitrkl.ac.in](mailto:sarkars@nitrkl.ac.in). The application form is available in the following link: [http://nitrkl.ac.in/oldwebsite/Jobs\\_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64\(2\).pdf](http://nitrkl.ac.in/oldwebsite/Jobs_Tenders/5ProjectFellowships/Doc/JRF%20LS-PND-64(2).pdf) which must be filled by candidates and also required to attach photocopies of all supporting documents, research papers (if any) etc.

Candidates will build a **Single PDF** file; it is to be sent to Principal Investigator ([sarkars@nitrkl.ac.in](mailto:sarkars@nitrkl.ac.in)) **on or before 31.05.2023, 2359 HRS**. The candidates are also required to produce relevant documents mentioned in the application form [such as original of all mark sheets and certificates, research papers (if any), experience certificate (if any) etc.] at the time of interview / joining. Selection / Joining will be canceled in case of any suppression of information / document provided earlier.

Mere possession of minimum qualification does not guarantee invitation to the interview. Candidates will be short listed based on merit and need of the project.

**Sd/-  
Asst. Registrar (SR)**

Copy to:

- 1) All Heads of the Departments, NIT Rourkela for publication on departmental notice boards.
- 2) Prof. Santanu Sarkar, PI with a request to give wide publicity to this advertisement.
- 3) Head of the Department, Electronics and Communication Engineering (EC).
- 4) Project file.